

## ABSTRACT

A FIFO memory having multiple buffer areas formed therein, and boundary pointers for defining the size of each buffer area. The FIFO memory also includes a controller operable for dynamically varying the value of each boundary pointer in accordance with the amount of incoming data to be stored in a given buffer area during the operation of the device so as to allow for maximum utilization of the memory space.

WDC99 313653-1 057457 0015